



DFW

SKYWKS.0027P

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Akbar Ali, et al.) Group Art Unit: 2681
App. No. : 10/821,531)
Filed : April 9, 2004)
For : HIGH AGILITY FREQUENCY)
SYNTHESIZER PHASE-LOCKED)
LOOP)
Examiner : UNKNOWN)

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

August 18, 2004

Chad W. Miller, Reg. No. 44,943

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. This Information Disclosure Statement is being filed before the mailing of the first Office Action on the merits, and as such, no fee is required in accordance with 37 C.F.R. § 1.97(b)(3).

Respectfully submitted,

Dated: 8/19/04

By: C. Miller

Chad W. Miller
Weide & Miller, Ltd.
Registration No. 44,943
Bank West Building, 5th Floor
7251 W. Lake Mead Blvd., Suite 530
Las Vegas, NV 89128
Tel. (702)-382-4804 (Pacific Time)

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. SKYWKS.0027P	APPLICATION NO. 10/821,531
	APPLICANT Akbar Ali, et al.	
	FILING DATE April 9, 2004	GROUP 2681

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	Tony van Roon, "Phase-Locked Loops Tutorial", Copyright © 2001, Last update: June 4, 2003, pp. 1-11	
	Ron Bertrand, "Supplementary Reading for 27 - Oscillators - The Basics of PLL Frequency Synthesis", Online Radio & Electronics Course, Copyright © 1999-2002, Last revision: April 2002, pp. 1-9	
	"Technical Brief SWRA029 - Fractional/Integer-N PLL Basics", Texas Instruments Wireless Communication Business Unit, August 1999, pp. 1-9 and 23-35	
	Daniel Abramovitch, "Phase-Locked Loops: A Control Centric Tutorial", Agilent Laboratories, 2002, pp. 1-15	
	Danny Abramovitch, "Phase-Locked Loops: A Control Centric Tutorial", Agilent Laboratories, May 8, 2002, pp. 1-20 and 31-34	

EXAMINER	DATE CONSIDERED
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	